

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently amended) A generator of a scanning velocity modulation deflection signal, comprising:

a variable conduction device comprising a transistor, said transistor coupled to said generator and having a first input responsive to a negative feedback signal, and said transistor having a second input responsive to a control signal;

in a first condition said transistor providing a path for receiving said negative feedback signal at said first input for controlling said scanning velocity modulation deflection signal in magnitude; and,

in a second condition said transistor interrupting said path and substantially inhibiting generation of said scanning velocity modulation deflection signal in response to said second input receiving said control signal.

2. (Previously presented) The generator of claim 1, wherein during said first condition said transistor varies conduction in accordance with a magnitude of said negative feedback signal.

3. (Currently amended) The generator of claim 2, wherein said transistor varies conduction to variably attenuate said scanning velocity modulating signal in accordance with said ~~[[said]]~~ negative feedback signal magnitude.

4. (Previously presented) The generator of claim 1, wherein during said second condition said transistor is fully conductive responsive to said control signal for substantially inhibiting said scanning velocity modulation deflection signal.

5. (Previously presented) The generator of claim 1, wherein during said second condition said transistor is fully conductive, attenuating said negative

feedback signal and substantially inhibiting generation of said scanning velocity modulation deflection signal said.

6. (Currently amended) The generator of claim 1, wherein said second condition conduction in said transistor is unresponsive to said negative feedback signal ~~[[signal]]~~.

7. (Canceled)

8. (Previously presented) A generator of a scanning velocity modulation deflection signal, comprising:

a variable conduction device consisting of a single transistor, said transistor having a first terminal responsive to a scanning velocity modulation feedback signal, and a second terminal responsive to a control signal;

in a first condition said transistor providing a feedback path for said feedback signal for controlling a magnitude of said scanning velocity modulation deflection signal; and,

in a second condition said transistor interrupting said feedback path and substantially inhibiting generation of said scanning velocity modulation deflection signal.

9. (Previously presented) A scanning velocity modulation deflection signal generator, comprising:

a transistor coupled to said scanning velocity modulation deflection signal generator and operational as a common base amplifier for a feedback signal coupled for controlling a magnitude of said scanning velocity modulation deflection signal, and said transistor being operational as a common emitter amplifier for interrupting said feedback signal and substantially inhibiting generation of said scanning velocity modulation deflection signal.

10. (Previously presented) The generator of claim 1, wherein said negative feedback signal is representative of power dissipation in a scanning velocity modulation drive amplifier responsive to said scanning velocity modulation deflection signal.

11. (Currently amended) The scanning velocity modulation deflection signal generator of claim 8, wherein [[in]] said second condition a signal input to said generator is attenuated to substantially inhibit said scanning velocity modulation deflection signal generation.

12. (Previously presented) The scanning velocity modulation deflection signal generator of claim 9, wherein said negative feedback signal is representative of power dissipation in a scanning velocity modulation drive amplifier responsive to said scanning velocity modulation deflection signal.

13. (Previously presented) The scanning velocity modulation deflection signal generator of claim 9, wherein said common emitter amplifier substantially inhibits generation of said scanning velocity modulation deflection signal by attenuating a signal input to said generator.